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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	TBD 10/724,846
Filing Date	Herewith
First Named Inventor	Maher et al.
Art Unit	TBD
Examiner Name	TBD
Attorney Docket Number	P04461-009

Sheet 1

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U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
A		US- 6,088,807	07/11/00	Maher et al.	
A		US- 5,657,483	08/12/97	Kardach et al.	
A		US- 5,361,392	11/01/94	Fourcroy et al.	
A		US- 4,893,271	01/09/90	Davis et al.	
A		US- 4,907,150	03/06/90	Arroyo et al.	
A		US- 5,021,983	06/04/91	Nguyen et al.	
A		US- 5,025,387	06/18/91	Frane	
A		US- 5,036,493	07/30/91	Nielsen	
A		US- 5,070,450	12/03/91	Holman Jr. et al.	
A		US- 5,083,266	01/21/92	Watanabe	
A		US- 5,086,387	02/04/92	Arroyo et al.	
A		US- 5,167,024	11/24/92	Smith et al.	
A		US- 5,203,003	03/13/93	Donner	
A		US- 5,175,853	12/29/92	Kardach et al.	
A		US- 5,237,699	08/17/93	Little et al.	
A		US- 5,369,771	11/29/94	Gettel	
A		US- 5,390,350	02/14/95	Chung et al.	
A		US- 5,392,441	02/21/95	Brasher et al.	
A		US- 5,404,546	04/04/95	Stewart	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ³
		Country Code ⁴ Number ⁴ Kind Code ⁵ (if known)				
A		GB 2127999	04/30/86	Kitchen		
A		EPO 0167853	06/11/84	Compaq		
A		EPO 0201020	04/28/86	Honewell		
A		EPO 0242010	01/12/87	Int'l Business Machines		
A		EPO 0349726	05/05/89	Toppan		
A		JPO SHO59[1984]-167752	10/25/83	Nippon Denki		

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 Examiner Name TBD
 Attorney Docket Number P04461-D09

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<i>FE</i>		US- 5,410,711	04/25/95	Stewart	
<i>FE</i>		US- 5,432,946	07/11/95	Allard et al.	
<i>FE</i>		US- 4,590,553	05/20/86	Noda	
<i>FE</i>		US- 5,630,107	05/13/97	Camean et al.	
<i>FE</i>		US- 5,630,143	05/13/97	Maher et al.	
<i>FE</i>		US- 4,204,249	05/20/80	Dye et al.	
<i>FE</i>		US- 4,267,577	05/12/81	Hashimoto et al.	
<i>FE</i>		US- 4,293,927	10/06/81	Hoshii	
<i>FE</i>		US- 4,312,035	01/19/82	Greene	
<i>FE</i>		US- 4,316,247	02/16/82	Iwamoto	
<i>FE</i>		US- 4,317,180	02/23/82	Lies	
<i>FE</i>		US- 4,317,181	02/23/82	Teza et al.	
<i>FE</i>		US- 4,409,665	10/11/83	Tubbs	
<i>FE</i>		US- 4,417,320	11/22/83	Ei	
<i>FE</i>		US- 4,573,117	02/25/86	Boney	
<i>FE</i>		US- 4,615,005	09/30/86	Maejima et al.	
<i>FE</i>		US- 4,748,559	05/31/88	Smith et al.	
<i>FE</i>		US- 4,758,945	07/19/88	Remedi	
<i>FE</i>		US- 4,766,567	08/23/88	Kato	

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		Country Code ² Number ³ Kind Code ⁴ (if known)				
<i>FE</i>		JPO SHO58[1983]-205226	05/25/82	Fujitsu Ltd.		
<i>FE</i>		JPO HEI31[1991]-116210	10/12/81	Hitachiseisakusho KK		
<i>FE</i>		JPO SHO 55[1980]-47549	09/28/78	Nippon Texas Instruments		
<i>FE</i>		EPO 0451661A2	03/30/91	Matsuzaki		
<i>FE</i>		GB 2010551	12/19/77	Bendix		
<i>FE</i>		GB 2130765	11/20/82	SWF		

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First Named Inventor	Maher et al.
Art Unit	TBD
Examiner Name	TBD
Attorney Docket Number	PO4401-509

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		Number-Kind Code ² (if known)			
RE		US- 4,780,843	10/25/88	Tietjen	
RE		US- 4,796,235	01/03/89	Sparks et al.	
RE		US- 4,825,407	04/25/89	Loessel et al.	
RE		US- 4,841,440	06/20/89	Yonezu et al.	
RE		US- 4,851,987	07/25/89	Day	
RE		US- 4,381,552	04/26/83	Nocilini et al.	
RE		US- 4,698,748	10/06/87	Juzswick et al.	
RE		US- 4,980,836	12/25/90	Carter et al.	
RE		US- 5,142,684	08/25/92	Perry et al.	
RE		US- 5,218,704	06/08/93	Watts Jr.	
RE		US- 5,222,239	06/22/93	Rosch	
RE		US- 5,241,680	08/31/93	Cole et al.	
RE		US- 5,276,889	01/04/94	Shiraishi et al.	
RE		US- 5,355,503	10/11/94	Soffel et al.	
RE		US- 5,361,364	11/01/94	Nagashige et al.	
RE		US- 5,388,265	02/07/95	Volk	
RE		US- 6,343,363	01/29/02	Maher et al.	
RE		US- 6,233,691	05/15/01	Atkinson	
RE		US- 6,243,820	06/05/01	Rosch	

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		Country Code ² Number ³ Kind Code ⁴ (if known)				
RE		PCT WO 92/10032	11/26/90	Adaptive Solutions		
RE		EPO 0358423	09/04/89	Advanced Micro Defices		
RE		EPO 0363567	06/22/89	Int'l Business Machines		
RE		EPO 0385567	01/18/90	Amport		
RE		EPO 0425410A2	10/29/89	Watts		
RE		JPO 58182736	10/25/83	Fujitsu		

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First Named Inventor	Maher et al.
Art Unit	TBD
Examiner Name	TBD
Attorney Docket Number	P04461-D09

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		Number-Kind Code ² (if known)			
JD		US- 6,311,287	10/30/01	Dischler et al.	
JD		US- 6,397,340	05/28/02	Watts Jr. et al.	
JD		US- 2001/0001880	05/24/01	Watts Jr.	
JD		US- 5,560,017	09/24/96	Barrett et al.	
JD		US- 5,560,020	09/24/96	Nakatani et al.	
JD		US- 5,560,024	09/24/96	Harper et al.	
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JD		US- 5,842,029	11/24/98	Conary et al.	
JD		US- 5,935,253	08/10/99	Conary et al.	
JD		US- 6,006,336	12/21/99	Watts Jr. et al.	
JD		US- 6,029,249	02/22/00	Atkinson	
JD		US- 6,173,409	01/09/01	Watts Jr. et al.	
JD		US- 6,223,293	04/24/01	Foster et al.	
JD		US- 5,396,635	03/07/95	Fung	
JD		US- 5,418,969	05/23/95	Matsuzaki et al.	
JD		US- 5,428,790	06/27/95	Harper et al.	
JD		US- 5,452,401	05/08/04	Lin	
JD		US- 5,457,790	10/10/95	Iwamura et al.	

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
JD		EPO 0229692	07/22/87	Int'l Business Machines		
JD		PCT WO 92/21081	11/26/92	Zenith Data Systems Corp.		
JD		EPO 0368144	05/16/90	Fourcroy et al.		
JD		EPO 0172344	02/26/86	Katayama		

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JA		US- 4,481,581	11/06/84	Johnson	
JA		US- 4,545,030	10/01/85	Kitchin	
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JA		US- 4,835,737	05/30/89	Herrig et al.	
JA		US- 4,570,219	02/11/86	Shibukawa et al.	
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75		US- 5,481,731	01/02/96	Conary et al.	
75		US- 5,528,790	06/25/96	Curran	
75		US- 5,630,146	05/13/97	Conary et al.	
75		US- 5,637,932	06/10/91	Koreeda et al.	
75		US- 4,835,728	05/30/89	Si et al.	
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		Examiner Name	TBD
Sheet	7	of	7
		Attorney Docket Number	P04461-D09

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77		DESIGN AND ELECTRONIK, "186-Processors with Low Current Drain Modules," Vol. 23, November 5, 1991, p.8	
77		European Search Report dated July 28, 1993 for E/93302399	
77		INTEL, "Intel 82347 Power Management PC Peripheral," October 1990, pps. 1-55-1-80	
77		INTEL, "386 DX Microprocessor Programmer's Reference Manual" 1990, pp. 4-6 and 8-4.	
77		INTEL, 386 SL Microprocessor SuperSet, 386 SL Microprocessor System Design Guide 1990, Introduction to the SL Superset, Chapter 1, pp. 1-1 through 1-3, System and Power Management, Chapter 14, pp 14-1 through 14-23, September 1990.	

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Application Number	10/784,848
Filing Date	February 23, 2004
First Named Inventor	Robert Maher
Art Unit	2115
Examiner Name	Thomas C. Lee
Attorney Docket Number	P04461-D09(11461.00.0186)

Sheet 1 of 2

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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/784,846
		Filing Date	February 23, 2004
		First Named Inventor	Robert Maher
		Art Unit	2115
		Examiner Name	Thomas C. Lee
Sheet 2	of 2	Attorney Docket Number	P04461-D09(11461.00.0186)

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TS		Texas Instruments, TMS320C5x, User's Guide, Digital Signal Processing Products, June 1991: pp. 3-34 - 3-37, 3-51, 4-78, 4-79, C-10, C-11	

Examiner Signature		Date Considered	1/21/06
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